

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<b>APPLICANTS</b>	:	Han-Gu Sohn, et al.	<b>ATTY DOCKET NO.</b>	:	8729-226JHM/SS20062US
<b>SERIAL NO.</b>	:	10/732,868	<b>EXAMINER</b>	:	Dare, Ryan A.
<b>FILED</b>	:	Dec. 10, 2003	<b>ART UNIT</b>	:	2186
<b>TITLE</b>	:	<i>CIRCUITS AND METHODS FOR PROVIDING VARIABLE DATA I/O WIDTH FOR SEMICONDUCTOR MEMORY DEVICES</i>			

MAIL STOP: AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RESPONSE AFTER FINAL ACTION UNDER 37 CFR 1.116

SIR:

Applicants hereby respond to the Official Action of April 4, 2008 as follows.

The claims presently in the application are set forth beginning on page 2 of this Response.

The Remarks portion begins on page 9 of this Response.

OK TO ENTER: /RD/